

**AMENDMENT TO THE SPECIFICATION**

Please amend the paragraph starting on page 20, line 25 and ending on page 21, line 6 of the specification as follows:

An exemplary configuration of the selection circuit **120** using a CMOS transistor is shown in FIG. 8. In this case, only the first block **121** in the selection circuit **120** is shown. Each of the other blocks **122 124** and **123 122** has an equivalent configuration to that of the first block, and therefore, description thereof will be omitted. As shown in FIG. 8, the first driving signal **131a** from the first selection circuit **130** is output as the driving signal **121a** via a CMOS transistor including an n-type MOS transistor **162a** and a p-type MOS transistor **163a** connected in parallel to each other.